

Introduction

The JTAG TAP controller implements the JTAG Test Access Port protocol. It has the IEEE interface on one side (TDI, TDO, TMS, TCK and TRST) and a generic interface on the other side (read/write, data, etc.).

Deliverables

- VHDL source code or encrypted source code depending upon license.
- Verification environment.

For further details, please contact Octera.

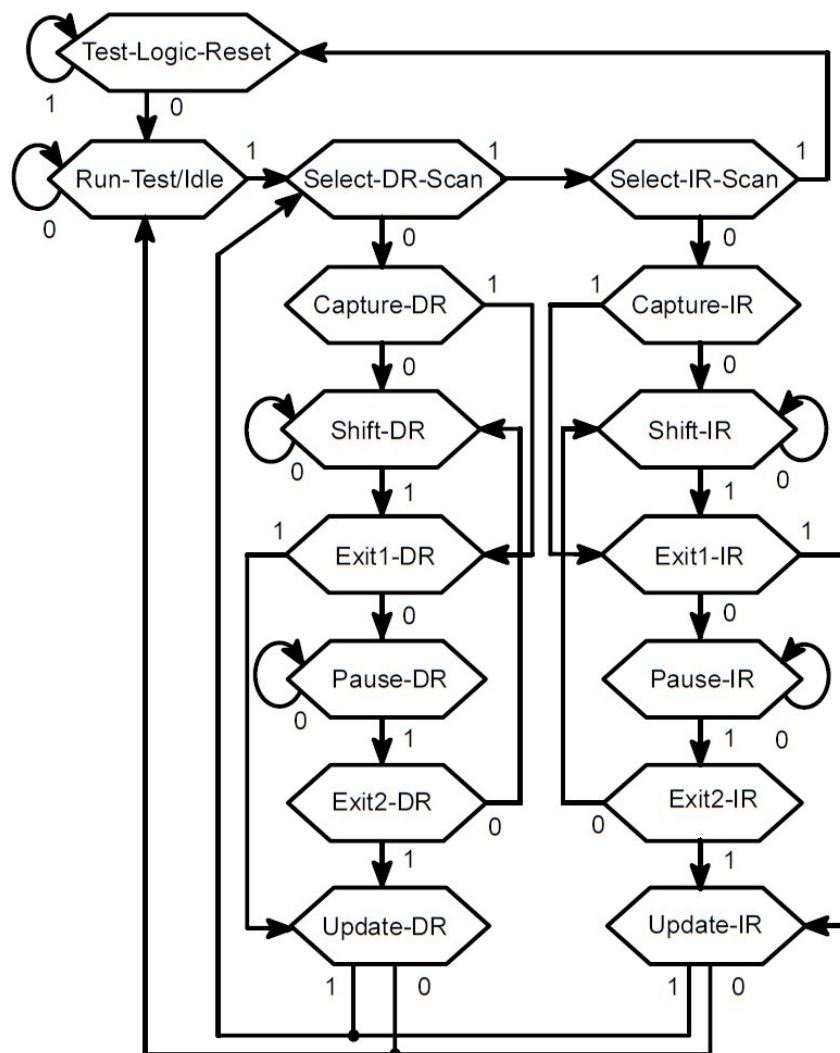


Figure 1

Product code: OCT-JTAG-TAP